



123003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Bheem Patel et al.

Title: VARIABLE-DELAY SIGNAL GENERATORS AND METHODS OF OPERATION THEREFOR

Attorney Docket No.: 884.936US1

Customer No.: 21186

031088 U.S.PTO
10/751135

123003

PATENT APPLICATION TRANSMITTAL**MAIL STOP PATENT APPLICATION**

Commissioner for Patents

P.O. Box 1450

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We are transmitting herewith the following attached items and information (as indicated with an "X"):

- Return postcard.
- Utility Patent Application under 37 CFR 1.53(b) comprising:
- Specification (31 pgs, including claims numbered 1 through 25 and a 1 page Abstract).
- Informal Drawing(s) (8 sheets).
- Partially signed Combined Declaration and Power of Attorney (8 pgs).
- Authorization to charge Deposit Account 19-0743 in the amount of \$1118.00 to pay the filing fee.
- Assignment of the invention to Intel Corporation (4 pgs) and Recordation Form Cover Sheet.
- Authorization to charge Deposit Account 19-0743 in the amount of \$40.00 to pay the Assignment recording fee.
- Information Disclosure Statement (2 pgs), Form 1449 (1 pgs) and copies of cited references (10).
- Communication Concerning Related Applications (1 pg.).

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	25-20	5	x 18.00 =	\$90.00
INDEPENDENT CLAIMS	6-3	3	x 86.00 =	\$258.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$770.00
TOTAL				\$1118.00

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

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S/N Unknown

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Bheem Patel et al. Examiner: Unknown
Serial No.: Unknown Group Art Unit: Unknown
Filed: Herewith Docket: 884.936US1
Title: VARIABLE-DELAY SIGNAL GENERATORS AND METHODS OF OPERATION
THEREFOR
Assignee: Intel Corporation Customer No.: 21186

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/605624, now 6,348,826	June 28, 2000	884.285US1	DIGITAL VARIABLE-DELAY CIRCUIT HAVING VOLTAGE-MIXING INTERPOLATOR AND METHODS OF TESTING INPUT/OUTPUT BUFFERS USING SAME
09/894486	June 28, 2001	884.513US1	CLOCK RECOVERY USING CLOCK PHASE INTERPOLATOR

Respectfully submitted,

BHEEM PATEL ET AL.

By Applicants' Representatives,

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